



#7  
10/30/02  
RTR

Docket No.: 1460.1016

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Yoshinobu KUBOTA, et al.

Serial No. 09/761,778

Group Art Unit: 2882

Confirmation No. 5961

Filed: January 18, 2001

Examiner: Chih Cheng G. Kao

For: OPTICAL CIRCUIT

**LETTER TO THE EXAMINER SUBMITTING  
CORRECTED AND/OR FORMAL DRAWINGS**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

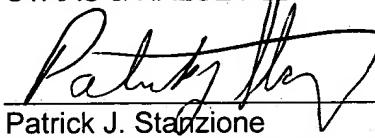
Applicants herewith submit eight sheets (FIGS. 1a-b, 2a-c, 3a-d, 4a-c, 5, 6a-f, 7a-c and 8a-d) of corrected and/or formal drawings for filing in the subject application.

It is respectfully requested that the corrected and/or formal drawings filed herewith be entered in the above-referenced application.

Respectfully submitted,

STAAS & HALSEY LLP

By:

  
Patrick J. Stanzone  
Registration No. 40,434

Date: Oct 8, 2002

700 11th Street, N.W., Ste. 500  
Washington, D.C. 20001  
(202) 434-1500

**CERTIFICATE UNDER 37 CFR 1.8(a)**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231

on October 8, 2002

STAAS & HALSEY LLP

By: Patrick J. Stanzone

Date: October 8, 2002

RECEIVED  
OCT 17 2002  
TECHNOLOGY CENTER 2800